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signature, embed, asymmetric, chipset, auther | Search | Advanced Search
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Web Results **1** - **10** of about **98** for <u>signature</u>, <u>embed</u>, <u>asymmetric</u>, <u>chipset</u>, <u>authentication</u> "<u>code module</u>".

Authenticated **code module** - Patent 20030126442
The **code module** of claim 2, wherein the **signature** comprises a digest value ... an
asymmetric key **embedded** in a hardware component of the computing device. ...
www.freepatentsonline.com/20030126442.html - 71k - <u>Cached</u> - <u>Similar pages</u>

aaa abdominal aortic aneurysm aaa access all areas
XPE eXtreme Performance Enhancement XPE [Microsoft Windows] XP **Embedded** XPF
eXPlosion-release Factor XPF eXtended Packet Filter XPG eXcellent Personal ...
www.xs4all.nl/~jtv/gtf/GPL_TLA_FAQ - 605k - <u>Cached</u> - <u>Similar pages</u>

[PDF] The Intel Safer Computing Initiative
File Format: PDF/Adobe Acrobat
key for the symmetric algorithm uses **asymmetric**, where the speed is not ... **chipset**-
specific authenticated **code module**. This module is tests for an ...
www.intel.com/intelpress/validation100/secc/SECC_100Validation.pdf - <u>Similar pages</u>

rootr.net : man : c (1)
Lets you **embed** Perl code into HTML documents. Perl code is evaluated at the server ...
ggFreeModule, ggGetSymbolAddress (3) - Portable **code module** loading ...
www.root-route.org/man/man/c++/1 - 886k - <u>Cached</u> - <u>Similar pages</u>

rootr.net : man : G(2)
Pod::Usage, pod2usage() (3p) - print a usage message from **embedded** pod ...
RSA_padding_check_none (3) - **asymmetric** encryption padding RSA_print, ...
www.root-route.org/man/man/G/2 - 250k - Supplemental Result - <u>Cached</u> - <u>Similar pages</u>

rootr.net : man : s(3p)
S (3o) - The output **signature** of the functor Hashtbl.Make. ... Lets you **embed** Perl code
into HTML documents. Perl code is evaluated at the server side and ...
www.rootr.net/man/man/s/3p - 811k - <u>Cached</u> - <u>Similar pages</u>

rootr.net : man : O(1)
... RSA_padding_check_none (3) - **asymmetric** encryption padding RSA_print, ... DECchip
21072/21071 Core Logic **chipset** apio (4/SPARC64) - SBus Aurora pio1 ...
www.rootr.net/man/man/O/1 - 250k - Supplemental Result - <u>Cached</u> - <u>Similar pages</u>

From horms at verge.net.au Sun Oct 1 04:42:01 2006 From: horms at ...
Also, it relies on clocks being synchronized, since **authentication** will fail if the UTC ... SPA
packets are non-replayable in both **asymmetric** and symmetric ...
lists.netfilter.org/pipermail/netfilter-devel/2006-October.txt - 694k - <u>Cached</u> - <u>Similar pages</u>

rootr.net : man : N(1)
... DSA_SIG_free (3) - allocate and free DSA **signature** objects DSA_do_sign, ...
RSA_padding_check_none (3) - **asymmetric** encryption padding RSA_print, ...
sshfs.net/man/man/N/1 - 250k - Supplemental Result - <u>Cached</u> - <u>Similar pages</u>

rootr.net : man : r(0)
... DECchip 21072/21071 Core Logic **chipset** apio (4/SPARC64) - SBus Aurora pio1 ...
auth_verify (3) - simplified interface to the BSD **Authentication** system ...
www.rroute.com/man/man/r/0 - 250k - Supplemental Result - <u>Cached</u> - <u>Similar pages</u>

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signature, embed, asymmetric, chips [Search]

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